

**Notice of References Cited**

Application/Control No.

09/893,023

Applicant(s)/Patent Under

Reexamination

O'MAHONY ET AL.

Examiner

A. Sefer

Art Unit

2826

Page 1 of 1

**U.S. PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Name          | Classification |
|---|---|--|-----------------|---------------|----------------|
|   | A | US-5,969,559                                     | 10-1999         | Schwartz      | 327/295        |
|   | B | US-5,387,885                                     | 02-1995         | Chi           | 333/100        |
|   | C | US-5,521,541                                     | 05-1996         | Okamura       | 327/297        |
|   | D | US-6,037,822                                     | 03-2000         | Rao et al     | 327/298        |
|   | E | US-6,005,428                                     | 12-1999         | Amdahl        | 327/161        |
|   | F | US-6,198,307                                     | 03-2001         | Garlepp et al | 326/83         |
|   | G | US-  |                 |               |                |
|   | H | US-  |                 |               |                |
|   | I | US-  |                 |               |                |
|   | J | US-  |                 |               |                |
|   | K | US-  |                 |               |                |
|   | L | US-  |                 |               |                |
|   | M | US-  |                 |               |                |

**FOREIGN PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Country | Name       | Classification |
|---|---|--|-----------------|---------|------------|----------------|
|   | N | JP 2-158165                                      | 06-1990         | Japan   | Sano et al |                |
|   | O |  |                 |         |            |                |
|   | P |  |                 |         |            |                |
|   | Q |  |                 |         |            |                |
|   | R |  |                 |         |            |                |
|   | S |  |                 |         |            |                |
|   | T |  |                 |         |            |                |

**NON-PATENT DOCUMENTS**

| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)                                      |
|---|---|--|
|   | U | Restle et al. "Desgning the Best Clock Distribution Network" IEEE Symposium on VLSI Circuits Digest of Techincal Papers, 1998. |
|   | V |  |
|   | W |  |
|   | X |  |

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.